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ERITFICATE OF plicant(s): Morimura	112857-474		
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Application No.	Filing Date	Examiner	Group Art Unit
10/790,450	February 27, 2004	I, Anya	2891
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being facsimile transn	nitted to the United States Patent	t and Trademark Office (Fax. N	No. <u>571-273-8300</u>
on January S	5, 2006		
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In Re Application Of: Morimura et al. Application No. Filing Date			TRANSMIT (General - F	Docket No. 112857-474						
10/799,450 February 27, 2004 I. Anya 29175 2891 9916 Title: SEMICONDUCTOR INTEGRATED CIRCUIT CARD MANUFACTURING METHOD AND SEMICONDUCTOR INTEGRATED CIRCUIT CARD MANUFACTURING METHOD AND BEST AVAILABLE COPY COMMISSIONER FOR PATENTS: Transmitted herewith is: Response (3 pgs.) In the above identified application. No additional fee is required. A check in the amount of is attached. The Director is hereby authorized to charge and credit Deposit Account No. 02-1818 as described below. Charge the amount of Credit any overpayment. Charge the amount of Payment by credit card. Form PTO-2038 is attached. WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card Information and authorization on PTO-2038. Thomas C. Basso (46,541) Cust. No. 29175 I hereby certify that this correspondence is belief deposited with the United States Postal Service sufficient postage as first class mail in an envel addressed to the "Commister for Patents, F.O. 1450, Alexandria, VA 22313-1450* [37 CFR 1.8(a)] on	In Re Application Of: Morimura et al.									
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Signature of Person Mailing Correspondence CC: Typed or Printed Name of Person Mailing Correspondence	Cus		_		I hereby certif deposited will sufficient posts addressed to 1450, Alexandri (Date	y that this correct the United State age as first class the "Commissioner in, VA 22313-1450"	es Postal Service with mail in an envelope for Patents, P.O. Box [37 CFR 1.8(a)] on			

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Applicants: Morimura et al. Appl. No.: 10/790,450

Conf. No.: 9916

Filed: February 27, 2004

Title: SEMICONDUCTOR INTEGRATED CIRCUIT CARD MANUFACTURING

METHOD AND SEMICONDUCTOR INTEGRATED CIRCUIT CARD

Art Unit: 2891

Examiner: Igwe Anya

Docket No.: 112857-474

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Mail Stop Issue Fee Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

RESPONSE

Sir:

In response to the USPTO Communication dated December 17, 2005, please amend the above-identified patent application as follows:

Amendments to the Specification begin on page 2 of this paper.

Remarks begin on page 3 of this paper.